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Li Mei Vermilya

Date: June 14, 2005

PATENT APPLICATION
Docket No. 2522-043
Client Ref. No. AW8089US/JY

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Han-Mei CHOI, et al.

Serial No.: 10/737,394

Examiner: Coleman, William D.

Filed: December 15, 2003

Art Unit: 2823

Confirmation No.: 7607

For:

METHOD FOR FORMING A MULTI-LAYERED STRUCTURE OF A
SEMICONDUCTOR DEVICE AND METHODS FOR FORMING A
CAPACITOR AND A GATE INSULATION LAYER USING THE MULTI-
LAYERED STRUCTURE

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- ☒ Publication (\$300) and Issue Fee (\$1400.00 large)
- ☒ In connection with issuance of a patent:
 - ☐ Supplemental Declaration
 - ☒ PTO Form 85B
- ☒ Applicant's Comments on Examiner's Statement of Reasons for Allowance
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees
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Customer No. 20575

Respectfully submitted,
MARGER JOHNSON & McCOLLOM, P.C.

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**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

In the Statement of Reasons for Allowance section, the Examiner recited portions of the allowed independent claims and stated that the prior art did not anticipate nor render obviousness as to the recited portions. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

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Respectfully submitted,
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